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International Application Number:				
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Title of Invention:	Interconnect-aware methodology for integrated circuit design			
First Named Inventor/Applicant Name:	Amir Alon			
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Filer:	Suzanne Erez/Hadas Bar-Gil			
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		IL920020007US1_OA30NO	64535	yes	9
	V07_RES04FEB08.pdf	14f1b7ebb79aa54be8c9439084dbf4db5 2c39a12	, 00		
	Multipa	rt Description/PDF files in	.zip description		
	Document Description		Start	End	
	Amendment After Final		1	1	
	Claims		2	4	
	Applicant Arguments/Remarks Made in an Amendment		5	9	
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Request for Continued Examination	IL920020007US1_RCE_04F	58271	no	3	
	(RCE)	EB08.pdf	5664c5cb62334a9eebc145e1a52dc9cc 80ebfa71	110	J
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New International Application Filed with the USPTO as a Receiving Office

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